

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	175	("same" with channel) and latch\$3.clm. and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 06:30
L2	1	"20050205880".PN.	US-PGPUB	OR	ON	2009/03/02 06:48
L3	1	"20040155698".PN.	US-PGPUB	OR	ON	2009/03/02 06:48
L4	1	"20040150611".PN.	US-PGPUB	OR	ON	2009/03/02 06:48
L5	1	"20040150611".PN.	US-PGPUB	OR	ON	2009/03/02 06:49
L6	1	"20030063079".PN.	US-PGPUB	OR	ON	2009/03/02 06:49
L7	1	"20030012330".PN.	US-PGPUB	OR	ON	2009/03/02 06:49
L8	1	"20020158828".PN.	US-PGPUB	OR	ON	2009/03/02 06:57
L9	1	"20010054999".PN.	US-PGPUB	OR	ON	2009/03/02 06:58
L10	1	"7019726".PN.	USPAT; USOCR	OR	ON	2009/03/02 06:58
L11	1	"7006068".PN.	USPAT; USOCR	OR	ON	2009/03/02 06:58
L12	1	"6989805".PN.	USPAT; USOCR	OR	ON	2009/03/02 06:59
L13	1	"6462596".PN.	USPAT; USOCR	OR	ON	2009/03/02 06:59
L14	1	"6396470".PN.	USPAT; USOCR	OR	ON	2009/03/02 07:03
L15	1	"6306694".PN.	USPAT; USOCR	OR	ON	2009/03/02 07:05
L16	1	"6306694".PN.	USPAT; USOCR	OR	ON	2009/03/02 07:05
L17	1	"6232798".PN.	USPAT; USOCR	OR	ON	2009/03/02 07:06
L18	1	"5815129".PN.	USPAT; USOCR	OR	ON	2009/03/02 07:06
L19	1	"4702560".PN.	USPAT; USOCR	OR	ON	2009/03/02 07:07

L20	1	"4625241".FN.	USPAT; USOCR	OR	ON	2009/03/02 07:07
L21	312	cmos and latch\$3.clm. and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:12
L22	59	cmos and latch\$3.clm. and "345"/\$.ccls. and (clocked adj inverter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:13
L23	158	latch.ab. and (clocked adj inverter).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:19
L24	69	channel.ab. and (clocked adj inverter). ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:26
L25	666	channel.ab. and (inverter).ab. and latch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:33
L26	16	l25 and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:33
L27	2574	345/98.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:37
L28	813	345/98.ccls. and inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:37
L29	687	345/98.ccls. and inverter and (clock\$4 or clk)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:38

L30	314	345/98.ccls. and inverter and (clock\$4 or clk) and transistor. clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:39
L31	310	345/98.ccls. and inverter and (clock\$4 or clk) and transistor. clm. and gate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:39
L32	261	345/98.ccls. and inverter and (clock\$4 or clk) and transistor. clm. and gate and (cmos or channel or nmos or pmos)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:40
L33	45093	(345/55-100 or 345/204-214 or 326/62-98 or 327/333). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:57
L34	2778	(345/55-100 or 345/204-214 or 326/62-98 or 327/333). ccls. and latch\$3.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:57
L35	1285	(345/55-100 or 345/204-214 or 326/62-98 or 327/333). ccls. and latch\$3.clm. and transistor.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:57
L36	939	(345/55-100 or 345/204-214 or 326/62-98 or 327/333). ccls. and latch\$3.clm. and transistor.clm. and gate.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:58
L37	466	(345/55-100 or 345/204-214 or 326/62-98 or 327/333). ccls. and latch\$3.clm. and transistor.clm. and gate.clm. and inverter. clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:58

L38	36	(345/55-100 or 345/204-214 or 326/62-98 or 327/333). ccls. and latch\$3.clm. and transistor.clm. and gate.clm. and inverter.clm. and (clocked adj inverter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 07:59
L39	140	((JUNICHI) near2 (YAMASHITA)).INV.	US-PGPUB; USPAT	OR	ON	2009/03/02 08:02
L40	135	((KATSUhide) near2 (UCHINO)).INV.	US-PGPUB; USPAT	OR	ON	2009/03/02 08:04
L41	1	(I39 or I40) and latch \$3.clm. and transistor.clm. and gate.clm. and inverter.clm. and (clocked adj inverter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 08:05
S1	1265	inverter.clm. and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/02/26 17:43
S2	331	inverter.clm. and "345"/\$.ccls. and clock.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/02/26 17:44
S3	156	inverter.clm. and "345"/\$.ccls. and clock.clm. and transistor.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/02/26 17:44
S4	123	inverter.clm. and "345"/\$.ccls. and clock.clm. and transistor.clm. and gate.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/02/26 17:46
S5	91	inverter.clm. and "345"/\$.ccls. and clock.clm. and transistor.clm. and gate.clm. and pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/02/26 17:46
S6	43	inverter.clm. and "345"/\$.ccls. and clock.clm. and transistor.clm. and gate.clm. and pixel and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/02/26 17:46

S7	87	inverter.clm. and "345"/\$.ccls. and clock.clm. and latch\$3.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/01 23:59
S8	24	inverter and "345"/\$.ccls. and clock and latch\$3.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 00:38
S9	311	("same" with channel) and latch\$3.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 00:48
S10	3	("same" with channel) and latch\$3.ti. and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/02 00:48

3/2/09 8:06:23 AM

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